

Application Number: 10/685,972

Filing Date: October 15, 2003

First Named Inventor: James D. Beasom

Group Art Unit: 2812

Examiner Name: Mulguri, Savitri

Attorney Docket: 33851/42502 DIV

FORM PTO-1449  
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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

## U.S. Patent Documents

Examiner Initials	Cite No.	U.S. Patent Number	Kind Code	Name of Patentee or Applicant of Cited Document	Date of Publication	Pages, Columns, Lines Where Relevant Passages or Figures Appear
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## Other Prior Art - Non Patent Literature Documents

Examiner Initials	Cite No	Name of Author (All Caps), Title of Article/Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country Where Published	T
GM		Sze, S.M. "Semiconductor Devices, Physics and Technology" John Wiley & Sons, Pg 384	

Examiner Signature	Date Considered
	4/5/05

\*\*EXAMINER: Initial citation considered. Draw line through citation if no in conformance and not considered. Include copy of this form with next communication to application.

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Examiner Initials	Cite No	Name of Author (All Caps), Title of Article/Item, Date, Page(s), Volume-Issue Number(s), Publisher, City and/or Country Where Published	T
<i>SM</i>		RUPIT PATEL, et al., A 30V Complementary Bipolar Technology on SOI for High Speed Precision Analog Circuits, 1997 IEEE BCTM, pgs. 48-50.	
<i>SM</i>		MC WILSON, et al., Process HJ: A 30 GHz NPN and 20 GHz PNP Complementary Bipolar Process for High Linearity RF Circuits, 1998 IEEE BCTM, pgs. 164-167.	

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*S. Mulpuri*

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